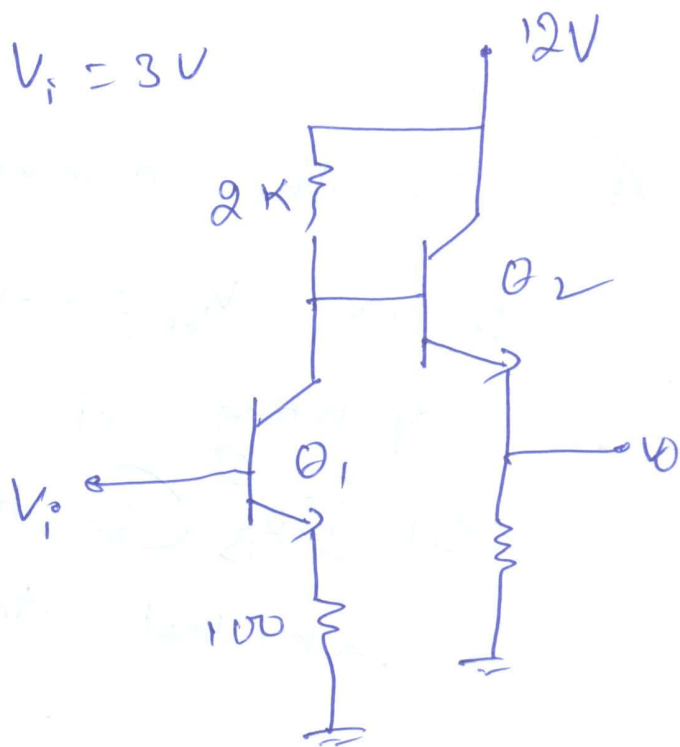


EDC - ASSIGNMENT

① Design a self bias ckt to operate a transistor at (8volt, 2mA). If transistor has $\beta = 200$, $V_{BE} = 0.7V$. Consider $V_{CC} = 16V$ and stability factor $S = 10$.

② In ckt shown, transistor have large β , and $V_{BE} = 0.7V$. Calculate o/p voltage if $V_i = 1V$ and if $V_i = 3V$



③ → A full wave rectifier with LC filter supplying current at 8mA with an RMS ripple of 1V. If line freq is 50Hz. Calculate the values of inductor and capacitor required. Suggest also appropriate transformer for the circuit.